### Understanding Computers Today and Tomorrow Comprehensive 14th Edition Morley Test Bank

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Chapter 2: The System Unit: Processing and Memory

#### **TRUE/FALSE**

1. The process of representing data in digital form so it can be used by a digital computer is called decimal byte representation.

ANS: F PTS: 1 REF: 52

2. The binary numbering system uses only two symbols—the digits 0 and 1—to represent all possible numbers.

ANS: T PTS: 1 REF: 52-53

3. Unlike ASCII, Unicode is a universal coding standard designed to represent text-based data written in any language, including those with different alphabets.

ANS: T PTS: 1 REF: 54

4. Each pixel in a monochrome graphic can be only one of two possible colors (such as black or white).

ANS: T PTS: 1 REF: 55

5. To convert analog sound to digital sound, several thousand samples—digital representations of the sound at a particular moment—are taken every second.

ANS: T PTS: 1 REF: 55

6. Early computers required programs to be written in machine language.

ANS: T PTS: 1 REF: 56

7. The main circuit board inside the system unit is called the megaboard.

ANS: F PTS: 1 REF: 57

8. The number of bits being transmitted at one time is dependent on the bus width.

ANS: T PTS: 1 REF: 62

9. ROM (read-only memory), also called main memory, is used to store the essential parts of the operating system while the computer is running.

ANS: F PTS: 1 REF: 62

10. Each location in memory has an address.

ANS: T PTS: 1 REF: 64

11. Traditionally, PC Cards were used for notebook expansion.

ANS: T PTS: 1 REF: 67

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- 12. The backside bus (BSB) has been one of the most common types of expansion buses in past years.
  - ANS: F PTS: 1 REF: 69
- 13. Several of the original ports used with desktop computers—such as the parallel ports traditionally used to connect printers—are now considered standard ports.

ANS: F PTS: 1 REF: 70

14. USB ports are used to connect a computer to a phone outlet via telephone connectors.

ANS: F PTS: 1 REF: 71

15. MIDI ports are used to receive wireless transmissions from devices.

ANS: F PTS: 1 REF: 71

16. The decode unit coordinates and controls the operations and activities taking place within the CPU.

ANS: F PTS: 1 REF: 74

17. The control unit takes the instructions fetched by the prefetch unit and translates them into a form that can be understood by the control unit, ALU, and FPU.

ANS: F PTS: 1 REF: 74-75

18. As a hard drive begins to get full, it takes less time to locate and manipulate the data stored on the drive.

ANS: F PTS: 1 REF: 77

19. Pipelining increases the number of machine cycles completed per second.

ANS: T PTS: 1 REF: 80

20. Typically, 3D chips are created by layering individual silicon wafers on top of one another.

ANS: T PTS: 1 REF: 85

### **MODIFIED TRUE/FALSE**

1. Most recent software programs, including the latest versions of Microsoft Windows, Mac OS, and Microsoft Office, use <u>ASCII</u>.

ANS: F, Unicode

PTS: 1 REF: 54

2. <u>ASCII</u> is the coding system traditionally used with personal computers.

ANS: T PTS: 1 REF: 54

3. Because of its large size, when audio data is transmitted over the Internet it is often <u>encrypted</u> to shorten the download time.

	ANS: F, compress	ed			
	PTS: 1	REF: 55			
4.	The <u>system unit</u> is t	the main case of a co	mputer.		
	ANS: T		PTS: 1	REF: 56	
5.				rcuitry and components that are particular to the particular to th	ckaged
	ANS: T		<b>PTS</b> : 1	REF: 58	
6.		led the microprocess	-	-does the vast majority of the proc	essing
	ANS: F, processor				
	PTS: 1	REF: 58			
7.	identical except for		h as the CPU) and n	on several computer systems that ar neasure how long each task takes ir nent being tested.	
	ANS: T		PTS: 1	REF: 60-61	
8.		ay is usually <u>external</u>			
8.		ay is usually <u>external</u>			
8.	Cache memory toda				
	Cache memory toda ANS: F, internal PTS: 1	REF: 61	cache.		
	Cache memory toda ANS: F, internal PTS: 1	REF: 61	cache.		
	Cache memory toda ANS: F, internal PTS: 1 <u>Memory</u> refers to th	REF: 61	cache.		
	Cache memory toda ANS: F, internal PTS: 1 <u>Memory</u> refers to th ANS: F, Storage	REF: 61 ne amount of long-te REF: 62	cache.		
	Cache memory toda ANS: F, internal PTS: 1 <u>Memory</u> refers to th ANS: F, Storage PTS: 1	REF: 61 ne amount of long-te: REF: 62	cache		

PTS: 1 REF: 63

11. The buses used to connect peripheral (typically input and output) devices to the motherboard are usually referred to as <u>expansion buses</u>.

ANS: T PTS: 1 REF: 68

12. Keyboards and mice are typically connected to a computers using parallel ports.

ANS: F, USB

PTS: 1 REF: 69

13. Today's CPUs contain hundreds of millions of transistors, and the number doubles approximately every 18 months, a phenomenon known as <u>Moliere's Law</u>.

ANS: F, Moore's Law

PTS: 1 REF: 73

14. Each machine language instruction in a CPU's instruction set is broken down into several smaller, machine-level instructions called <u>supercode</u>.

ANS: F, microcode

PTS: 1 REF: 75

15. With <u>pipelining</u>, a new instruction begins executing as soon as the previous one reaches the next stage of the pipeline.

ANS: T PTS: 1 REF: 80

### **MULTIPLE CHOICE**

1.	Eight bits grouped	together are collect	tively referred t	o as a
	a. kilobit		c. p	oixel
	b. byte		d. b	oinary
	ANS: B	PTS: 1	<b>REF:</b> 52	2

a. 2	2			с.	10
b. 5	5			d.	16
ANS	: C	PTS:	1	REF:	52

3. A \_\_\_\_\_ is the smallest unit of data that a binary computer can recognize.

a. byte b. datum		c. pixel d. bit
ANS: D	<b>PTS:</b> 1	REF: 52

4. A \_\_\_\_\_ is equal to 1,024 bytes.

	<ul><li>a. kilobyte (KB)</li><li>b. megabyte (MB)</li></ul>				gigabyte (GB) terabyte (TB)
	ANS: A	PTS:	1	REF:	52
5.	Each place value in a a. 0 b. 1	a binary	number repres	с.	raised to the appropriate power. 2 10
	ANS: C	PTS:	1	REF:	53
6.	With bitmapped ima image quality.	ges, the	color of each _	is :	represented by bits; the more bits used, the better the
	<ul><li>a. pixel</li><li>b. vector</li></ul>				map byte
	ANS: A	PTS:	1	REF:	54-55
7.	In a 16.8-million-col store the color data f a. mega color b. true color			age.	y or) image, three bytes (24 bits) are used to real color full color
	ANS: B	PTS:	1	REF:	55
8.	Like graphics data, _ in order to be stored a. pixel data b. giga data			or proce c.	ound of someone speaking—must be in digital form ssed by a PC. audio data audio programs
	ANS: C	PTS:	1	REF:	55
9.	collection of	home r	novies, feature	films, a	and television shows—is displayed using a
	<ul><li>a. slides</li><li>b. pixels</li></ul>			с. d.	vectors frames
	ANS: D	PTS:	1	REF:	56
10.	A(n) instructio specific operations a a. COBOL languag b. ASCII	nd stora		с.	ss string of 0s and 1s, but it actually represents programming language machine language
	ANS: D	PTS:	1	REF:	56
11.	are very small are embedded. a. Pixels b. Pentiums	pieces c	of silicon or oth	c.	conducting material onto which integrated circuits Chips Motherboards
	ANS: C	PTS:	1	REF:	56
12.	One measurement of (GHz).	the spe	ed of a CPU is	the	_, which is rated in megahertz (MHz) or gigahertz

a. system speed c. system rpm

	b. CPU clock speed	1		d.	CPU rpm
	ANS: B	PTS:	1	REF:	60
13.	A computer is time.	the amo	unt of data (me	asured	in bits or bytes) that a CPU can manipulate at one
	<ul><li>a. word</li><li>b. character</li></ul>			с. d.	statement unit
	ANS: A	PTS:	1	REF:	61
14.	A is an electron	nic path	over which dat		
	a. bus b. lane				word cache memory
	ANS: A	PTS:	1	REF:	62
15.	The bus width and b data that can be trans a. clock speed b. throughput			given p c.	machine cycle
		DTC	1		memory
	ANS: B	PTS:	1	REF:	62
16.	The term refers a. storage media b. memory	s to chip	-based storage	c.	/ the computer. hard drive Zip drive
	ANS: B	PTS:	1	REF:	62
17.	One of the most pror a. magnetoselective b. magnetobalance	e	ypes of nonvol	с.	AM is magnetic (or more precisely,) (MRAM). magnetoresistive magnetocharged
	ANS: C	PTS:	1	REF:	64
18.	are small comp a. ACs b. Fans	onents	• •	c.	luminum with fins that help to dissipate heat. Heat buses Heat sinks
	ANS: D	PTS:	1	REF:	65
19.	consists of non a. RAM b. Register	volatile	memory chips	c.	n be used for storage by the computer or the user. SDRAM Flash memory
	ANS: D	PTS:	1	REF:	65
20.	have begun to a. Motherboards b. Microprocessors	-	ROM for storir	с.	m information, such as a PC's BIOS. Adapter cards Flash memory chips
	ANS: D	PTS:	1	REF:	65
21.	The enables up	to 127	devices to be c	onnecte	ed to a computer through a single port on the

computer's system unit. a. HyperTransport bus c. AGP (Accelerated Graphics Port) bus

	b. USB standard			d.	PCI Express Bus
	ANS: B	PTS:	1	REF:	69
22.	Most network cards but is larger. a. RJ-11 connector b. RJ-12 connector		a port that acce	с.	), which looks similar to a telephone connector RJ-14 connector RJ-45 connector
	ANS: D		1	REF:	
23.					USB port to convert one port into several USB
23.	ports.		i plugs into you		
	a. hub b. module				bus connector
	ANS: A	PTS:	1	REF:	71
24.	Most computers toda devices as soon as th a. Plug and Play b. Match			e PC is c.	n which the computer automatically configures new powered up. Serial port Parallel port
	ANS: A	PTS:	1	REF:	71
25.	The key element of t like a switch control a. processor b. transistor ANS: B	ling the		ons insid c. d.	chipbus S-card
26.	<ul><li>can understand.</li><li>a. register</li><li>b. decode unit</li></ul>			c. d.	and translates them into a form that the control unit ALU internal cache
	ANS: B	PTS:	1	REF:	73
27.	The is the section a. FPU b. control unit	on of th	e CPU that per	с.	rithmetic involving integers and logical operations. decode unit ALU
	ANS: D	PTS:	1	REF:	74
28.	The orders data a. ALU b. prefetch unit	a and in	structions from		or RAM based on the task at hand. control unit decode unit
	ANS: B	PTS:	1	REF:	75
29.	order to help avoid c a. control unit b. floating point un	lelays ir iit	n processing.	c. d.	s will be needed and retrieves them ahead of time, in arithmetic/logic unit prefetch unit
	ANS: D	PTS:	1	REF:	15

30.	Instructions and data a. control unit	flow in	and out of the CI	c.	ia the decode unit bus interface unit	
	b. prefetch unit	500				
	ANS: D	PTS:	I R	EF:	75	
31.	In order to synchroni motherboard—is use		a computer's op	eratio	ions, aa small quartz crystal located o	on the
	a. cycle chip				system clock	
	b. fetch unit			d.	microprocessor	
	ANS: C	PTS:	1 R	EF:	75	
32.	Some must be a	added in	pairs for optimal	l perf	formance.	
	a. interfaces			-	USB ports	
	b. memory modules	S		d.	hard drives	
	ANS: B	PTS:	1 R	EF:	77	
33.	Today's CPUs are fo materials.	rmed us	ing a process call	led _	that imprints patterns on semiconductor	
	a. vectoring			с.	serigraphy	
	b. lithography				imprintment	
	ANS: B	PTS:	1 R	EF:	79	
34.	One nanometer (nm)	is	of a meter.			
	a. one-billionth				one-thousandth	
	b. one-millionth			d.	one-tenth	
	ANS: A	PTS:	1 R	EF:	79	
35.	Terascale computing second (teraflops).	is the al	bility of compute	rs to	process one floating-point operations p	ber
	a. million			c.	trillion	
	b. billion			d.	quadrillion	
	ANS: C	PTS:	1 R	EF:	85	
	Case-Based Critical	l Thinki	ng Questions			
	Case 2-1					
		no has in	et hought a new	comn	puter. Now she has to determine how to conn	act this
	computer to the devi					
36.	To connect her extern the port.	nal hard	drive where her	musio	ic files are stored to the computer, Jess needs	to use
	a. serial			c.	network	
	b. USB			d.	modem	
	ANS: B	PTS:	1 R	EF:	71 TOP: Critical Thinking	
37.	Jess has pictures from	n her old	d computer saved	on a	a flash drive. To transfer these to her new con	nputer,

she would use a(n) \_\_\_\_ port. a. SCSI c. modem

b. FireWire		d. USB	
ANS: D	PTS: 1	REF: 71	TOP: Critical Thinking

# **Case-Based Critical Thinking Questions**

## Case 2-2

Jack has a computer at home that he uses to access the Internet, store and edit personal photos, and create and edit documents. Recently, he has come to realize that in order to keep the computer performing at its best, he needs to carry out regular system maintenance on the computer.

38.	Jack can speed up his a. RAM b. Flash drives	s computer by scann	ing it for c. d.	spyware	·	
	ANS: C	PTS: 1	REF:	78	TOP:	Critical Thinking
39.	Jack can use the browsing history, and			e temporary file	es, such	as installation files, Web
	a. Windows Registr	ry	с.	Temporary F	iles	
	b. Disk Defragment	•		Windows Dis		nup
	ANS: D	PTS: 1	REF:	78	TOP:	Critical Thinking
40.		select the optio	on on the	Fools tab to che Defragment r	eck that	Windows Explorer, select hard drive for errors.
	ANS: A	PTS: 1	REF:	78	TOP:	Critical Thinking

## COMPLETION

1.	data	consists	of still	images.	such as	photograp	ohs or	drawings	s.

ANS: Graphics

PTS: 1 REF: 54

2. One of the most common methods for storing graphics data is in the form of a bitmap—a grid of hundreds of thousands of dots, called \_\_\_\_\_\_.

ANS: pixels

PTS: 1 REF: 54

3. Text-based data is represented by fixed-length binary coding systems specifically developed for text-based data—namely, ASCII, EBCDIC, and \_\_\_\_\_.

ANS: Unicode

PTS: 1 REF: 53

4. A(n) \_\_\_\_\_\_ is a thin board containing chips and other electronic components.

ANS: circuit board

PTS: 1 REF: 56

5. \_\_\_\_\_\_ are collections of electronic circuits containing microscopic pathways along which electrical current can travel.

ANS: ICs Integrated circuits Integrated circuits (ICs) ICs (Integrated circuits)

PTS: 1 REF: 56-57

6. The power supply inside a desktop computer connects to the \_\_\_\_\_\_ to deliver electricity to the computer.

ANS: motherboard

PTS: 1 REF: 57

7. Most CPUs today are \_\_\_\_\_ CPUs; that is, CPUs that contain the processing components or cores of multiple independent processors on a single CPU.

ANS: multi-core

PTS: 1 REF: 58

8. \_\_\_\_\_\_ is a special group of very fast memory circuitry located on or close to the CPU.

ANS: Cache memory

PTS: 1 REF: 61

9. Like the CPU, RAM consists of circuits etched onto chips. These chips are arranged onto circuit boards called \_\_\_\_\_\_.

ANS: memory modules

PTS: 1 REF: 63

10. \_\_\_\_\_\_ are locations on the motherboard into which expansion cards can be inserted to connect those cards to the motherboard.

ANS: Expansion slots

PTS: 1 REF: 66

11. Expansion buses connect directly to \_\_\_\_\_\_ on the system unit case or to expansion slots on the motherboard.

ANS: ports

PTS: 1 REF: 68-69

12. \_\_\_\_\_\_ are the connectors located on the exterior of the system unit that are used to connect external hardware devices.

ANS: Ports

PTS: 1 REF: 70



13. The accompanying figure shows the mouse connected to the computer via a(n)

ANS: USB hub

PTS: 1 REF: 71

14. \_\_\_\_\_ computing utilizes atoms or nuclei working together as quantum bits that are capable of representing more than just two states as in electronic computing of today.

ANS: Quantum

PTS: 1 REF: 83

15. A(n) \_\_\_\_\_\_\_ slot can be used with both the postage-stamp-sized Secure Digital (SD) flash memory cards, as well as with peripheral devices adhering to the Secure Digital Input/Output (SDIO) standard.

ANS: SD

PTS: 1 REF: 72

16. The \_\_\_\_\_\_ coordinates and controls the operations and activities taking place within the CPU, such as retrieving data and instructions and passing them on to the ALU or FPU for execution.

ANS: control unit

PTS: 1 REF: 74-75

17.		ay can process more than one piece of microcode at one time—a characteristic, or being able to process multiple instructions per cycle (IPC).
	ANS: superscalar	
	PTS: 1	REF: 76
18.		are stored, retrieved, and then stored again, they often become
	ANS: fragmented	
	PTS: 1	REF: 77
19.		are tiny, hollow tubes made up of carbon atoms.
	ANS: CNT Carbon nanotubes Carbon nanotubes ( CNT (Carbon nanot	
	PTS: 1	REF: 82
20.	The	chip is estimated to be 100 times faster than silicon.
	ANS: graphene	
	PTS: 1	REF: 80

## ESSAY

1. Explain what a register is and how it is used.

### ANS:

A register is high-speed memory built into the CPU. Registers are used by the CPU to temporarily store data and intermediary results during processing. Registers are the fastest type of memory used by the CPU, even faster than Level 1 cache. Generally, the more data a register can contain at one time, the faster the CPU performs.

PTS: 1 REF: 65 TOP: Critical Thinking

2. Of what does ROM (read-only memory) consist? What is one important difference between ROM and RAM (random access memory)?

ANS:

ROM (read-only memory) consists of nonvolatile chips that permanently store data or programs. Like RAM, these chips are attached to the motherboard inside the system unit, and the data or programs are retrieved by the computer when they are needed. An important difference, however, is that you can neither write over the data or programs in ROM chips (which is the reason ROM chips are called *read-only*), nor destroy their contents when you shut off the computer's power.

PTS: 1 REF: 65 TOP: Critical Thinking

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3. What are the general operations of a machine cycle?

ANS:

Each machine cycle consists of the following four general operations:

- 1. Fetch—the program instruction is fetched.
- 2. Decode—the instructions are decoded so the control unit, ALU, and FPU can understand them.
- 3. Execute—the instructions are carried out.

4. Store—the original data or the result from the ALU or FPU execution is stored either in the CPU's registers or in memory, depending on the instruction.

PTS: 1 REF: 76 TOP: Critical Thinking

4. Explain the difference between multiprocessing and parallel processing.

ANS:

With multiprocessing, each CPU typically works on a different job. Because multiple jobs are being processed simultaneously, they are completed faster than with a single processor. With parallel processing, multiple processors work together to make one single job finish sooner; a control processor assigns a portion of the processing for that job to each CPU.

PTS: 1 REF: 80-81 TOP: Critical Thinking

5. Describe how Hyper-Threading Technology works.

ANS:

Hyper-Threading Technology is a technology developed by Intel to enable software to treat a single processor as two processors. Since it utilizes processing power in the chip that would otherwise go unused, this technology lets the chip operate more efficiently, resulting in faster processing, provided the software being used supports Hyper-Threading.

PTS: 1 REF: 81 TOP: Critical Thinking